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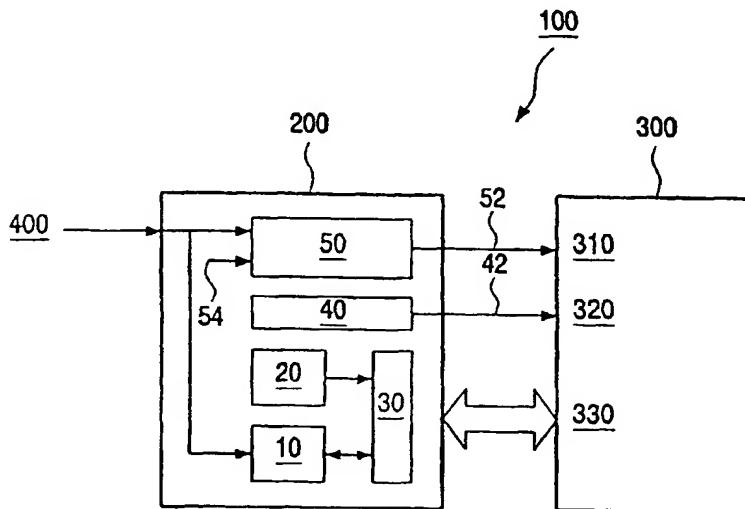
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(54) Title: METHOD AND BASE CHIP FOR MONITORING THE OPERATION OF A MICROCONTROLLER UNIT



WO 03/104993 A1

(57) Abstract: To enable a method and a base chip (200) for monitoring the operation of at least one microcontroller unit (300) that is intended for at least one application and is associated with a system (100) to be further developed in such a way that the operation of the microcontroller unit (300), which is associated with a serial system, is monitored in such a way as to enable faulty operation, and particularly faulty operation that discharges the battery, to be reliably prevented, it is proposed that the microcontroller unit (300) has at least one non-volatile memory area (10) associated with it, the memory area (10) can be read from and/or written to by the microcontroller unit (300), and at least one set of statistics, and in particular a set of fault statistics, relating to the operation of the microcontroller unit (300), can be kept by means of the memory area (10).

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